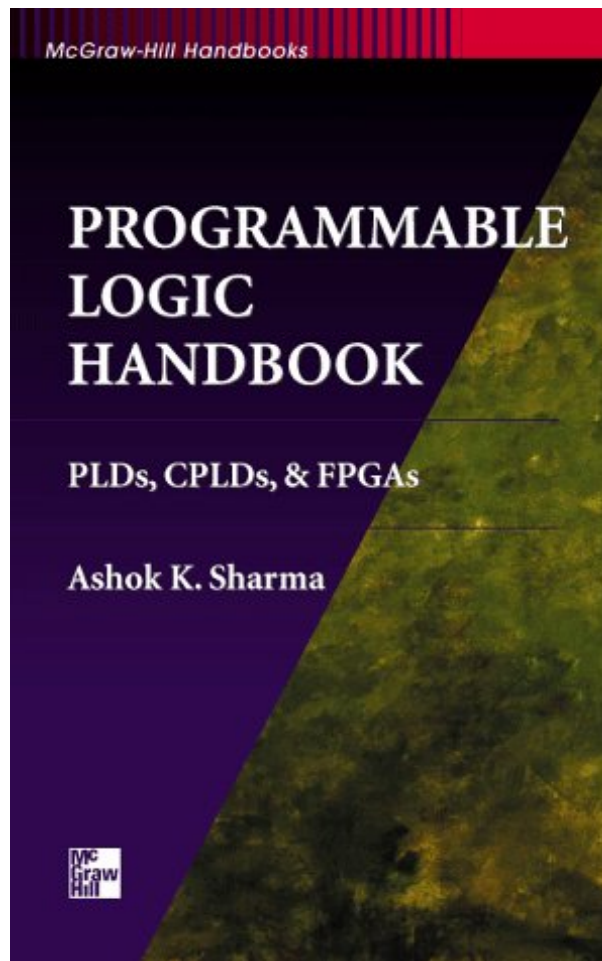# PROGRAMMABLE LOGIC HANDBOOK: PLDS, CPLDS AND FPGAS BY ASHOK K. SHARMA

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A Handbook and Information Resource Guide for Selection, Design, and Testing of PLDs, CPLDs, and FPGAs. PLDs are being used everywhere. Programmable logic devices are at the heart of the difital hardware and software revolution that experts predict will play a major role in every aspect of our lives in the new millennium. Siple and complex PLDs including FPGAs are at work in cellular phones, video games, as well as advanced military and space satellite systems. They'er in microwave ovens, automobiles, VCRs, television sets, disk drives, network hubs, bridgers, routers, laptops, and many other products. These user-configurable "mini-computers" save board space, use less power, offer quicker turn-around time, and provide more flexibility than the tranditional integrated circuits assembly approach. PLD users, designers, and application engineers will appreciate the in-depth coverage of the latest technology, architectures, design and development tools information, and resources, From simple PLD applications to highly complex devices in custom computing and space hardware designs, this handbook helps you make the right technical decisions\=and win the time-to-market race. It's an essential guidebook fro those who want to be on the leading edge of electronic frontier exploration in the new millennium.

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This practice-oriented guide to programming with Field Programmable Logic Devices is the most complete resource on the subject. FPLDs are an essential part of today's high-performance electronic systems because they save board space, use less power, and offer quicker turnaround times than traditional integrated circuits. However, to maximize FPLDs, designers must understand and get around the tradeoffs involved. This one-stop guide addresses the challenges and opportunities through detailed coverage of: FPGAs, PLDs, PLAs, and CPLDs; the high-level description languages VHDL and Verilog; test issues; and more.

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